

**Search Notes**

Application/Control No.

10/773,050

Examiner

Peter Coughlan

Applicant(s)/Patent under  
Reexamination

SALAM ET AL.

Art Unit

2129

**SEARCHED**

Class	Subclass	Date	Examiner
706	33	9/27/2007	PDC
706	15	9/27/2007	PDC
706	40	9/27/2007	PDC

**INTERFERENCE SEARCHED**

Class	Subclass	Date	Examiner

**SEARCH NOTES  
(INCLUDING SEARCH STRATEGY)**

	DATE	EXMR
East--capacitor, quadrant multipliers, transconductance, amplifiers, load resistance, d flip flops, neural networks, Waheed,	9/27/2007	PDC
East--II--Fathi, Erten, feed forward, VLSI, medium, transmission, communication, databus, weights, training, memory, storage,	9/27/2007	PDC
East--III--pattern comparison, analog, digital, continuous time, substrate, interconnection, one dimension, pulsed NN	9/27/2007	PDC
East--IV--cell neuron, capacitor, feed forward, circuit	9/27/2007	PDC
IEEE--Fathi M. Salam, Khurram Waheed, VLSI, capacitor, quadrant multiplier, d flip flops, bi stable multivibrator, substrate,	9/27/2007	PDC
Inventors Fathi M. Salam Khurram Waheed	9/27/2007	PDC